

Mapping 1 (bit-mapping order: $i_1q_1i_2q_2$)

FIG. 1

WO 2004/036818 PCT/EP2002/011694

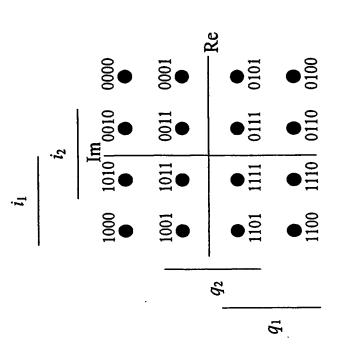
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		<i>i</i> ₂			
		\vec{i}_1			
		0010	1010 •	1000 •	0000
		0110 •	1110	1100	0100 •
q_2	q_1	0111	1111	1101	• Re 0101
		0011	1011	1001	0001

Mapping 2 (bit-mapping order: $i_1q_1i_2q_2$)

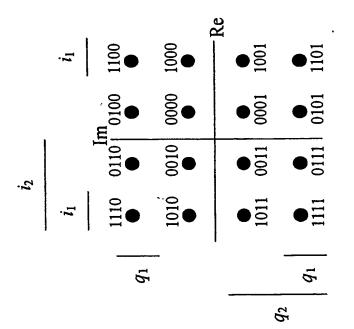
FIG. 2

FIG. 3B



Mapping 3 (bit-mapping order: i1q1i2q2)

Mapping 4 (bit-mapping order: i1q1i2q2)



.IG. 3A

